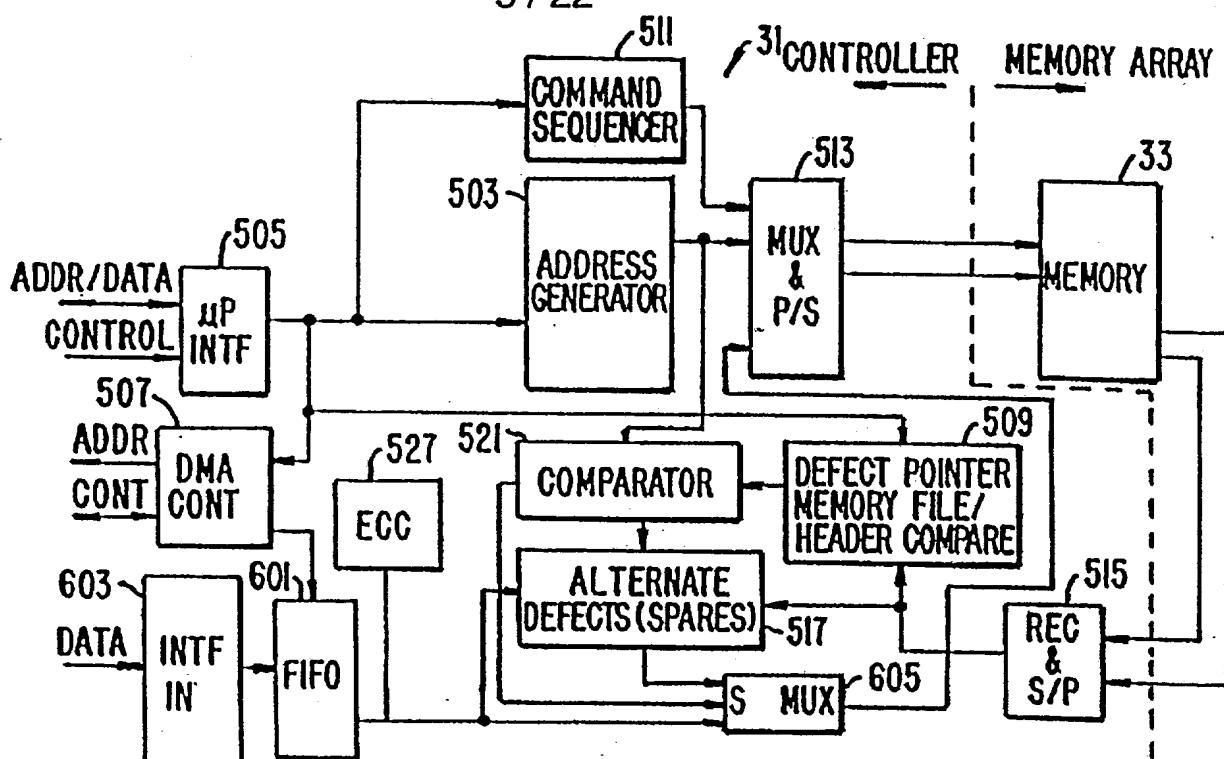


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WRITE DATA PATH CONTROL

FIG._7

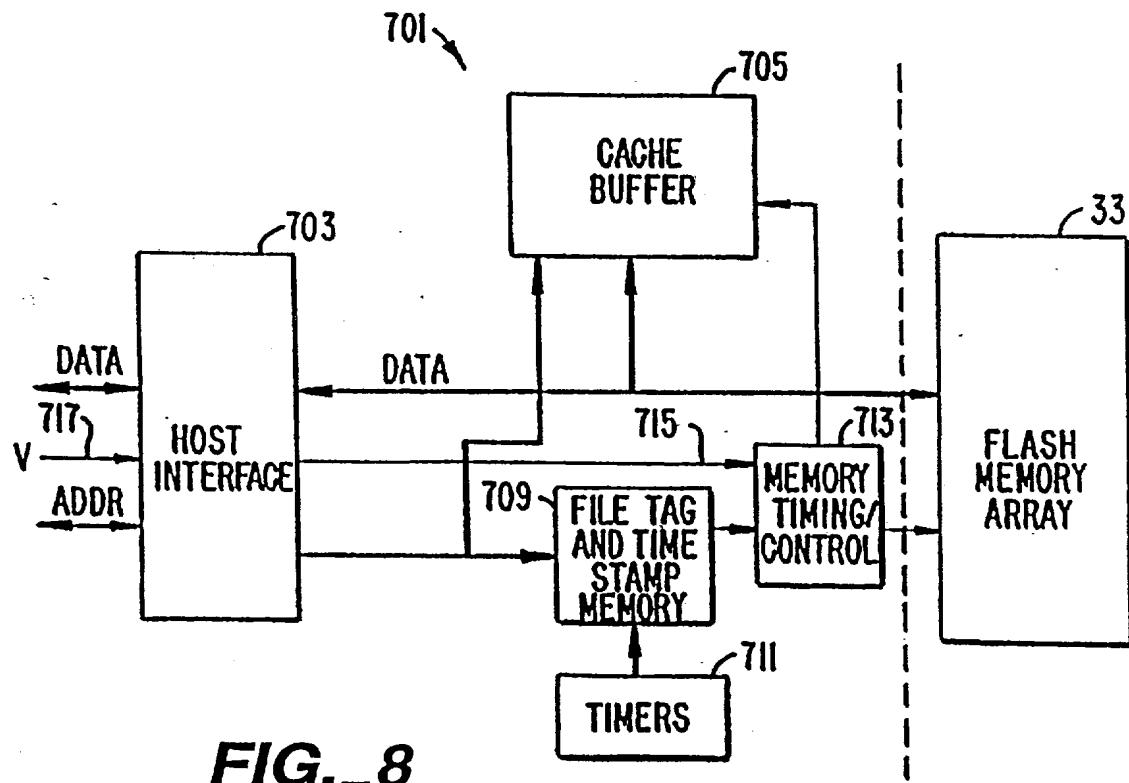


FIG._8

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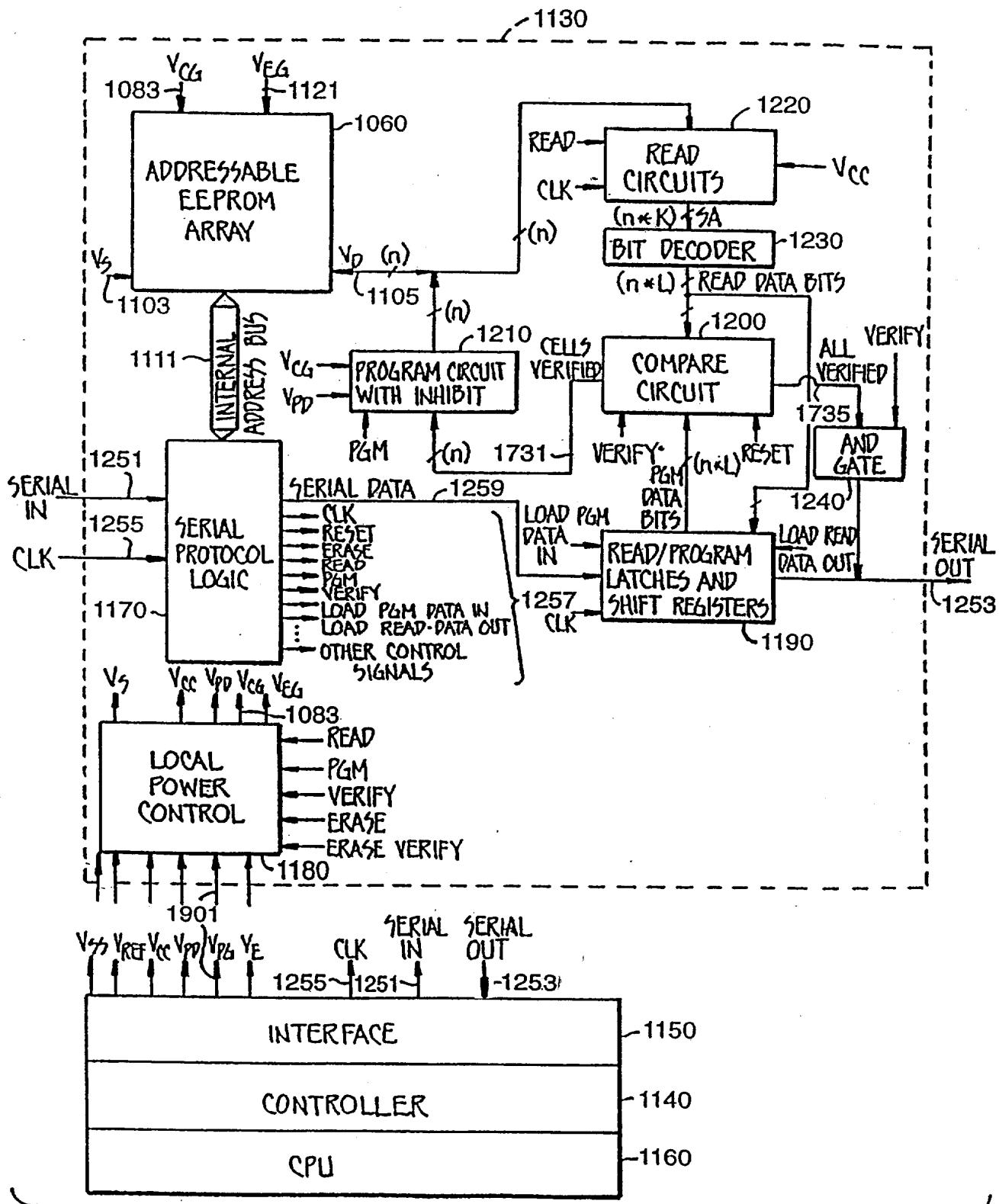
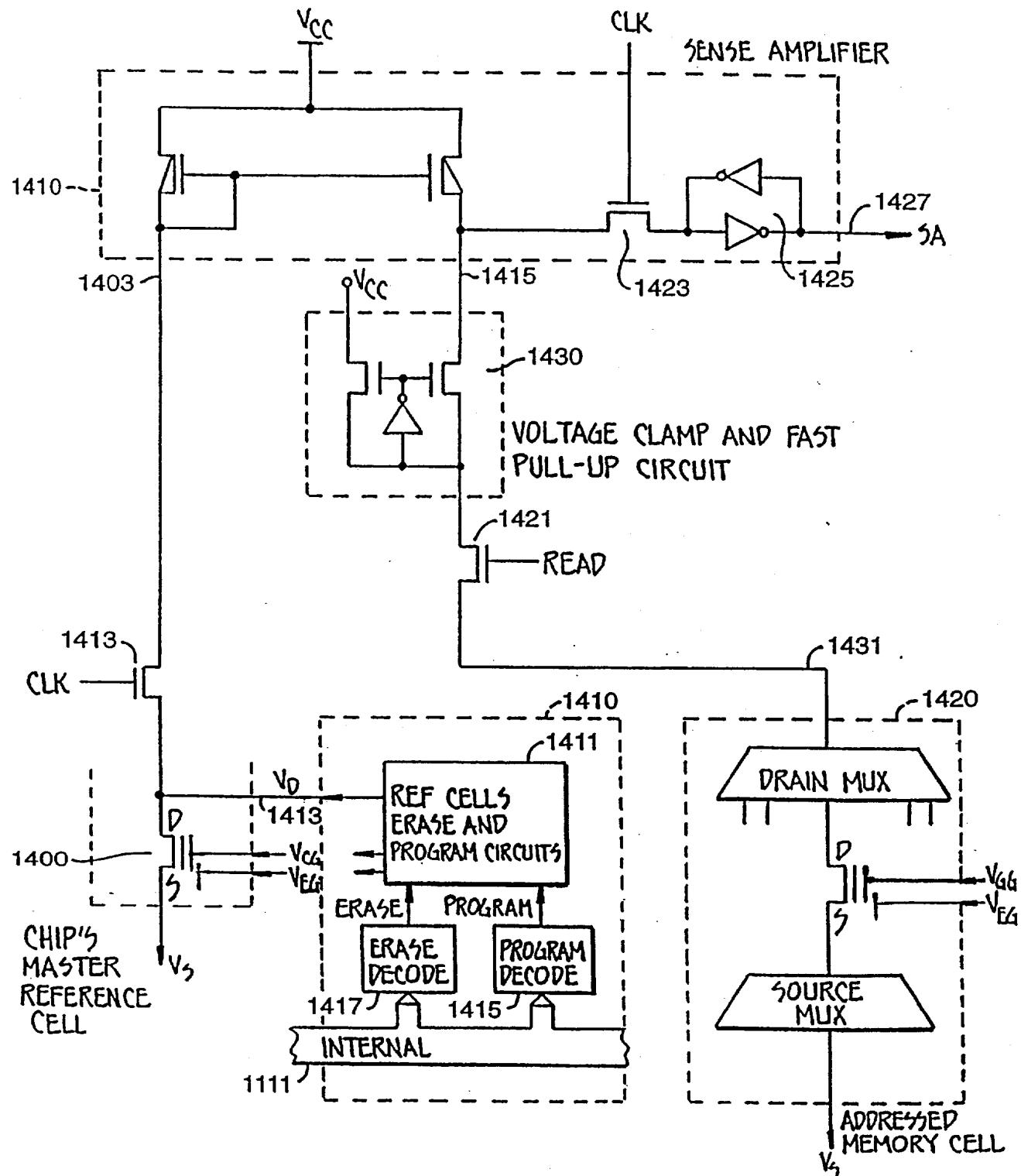


FIG. - 13

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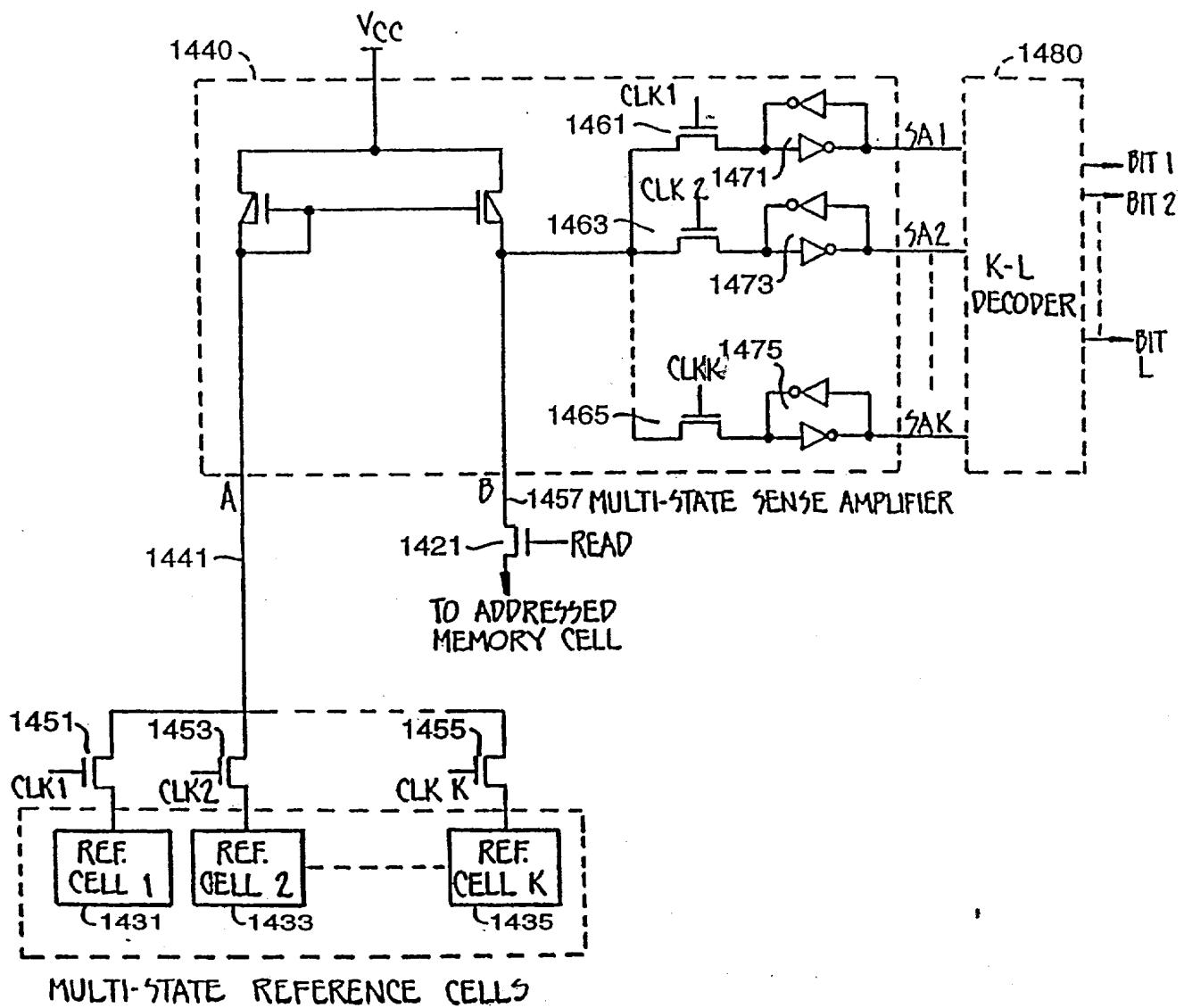


FIG._17B

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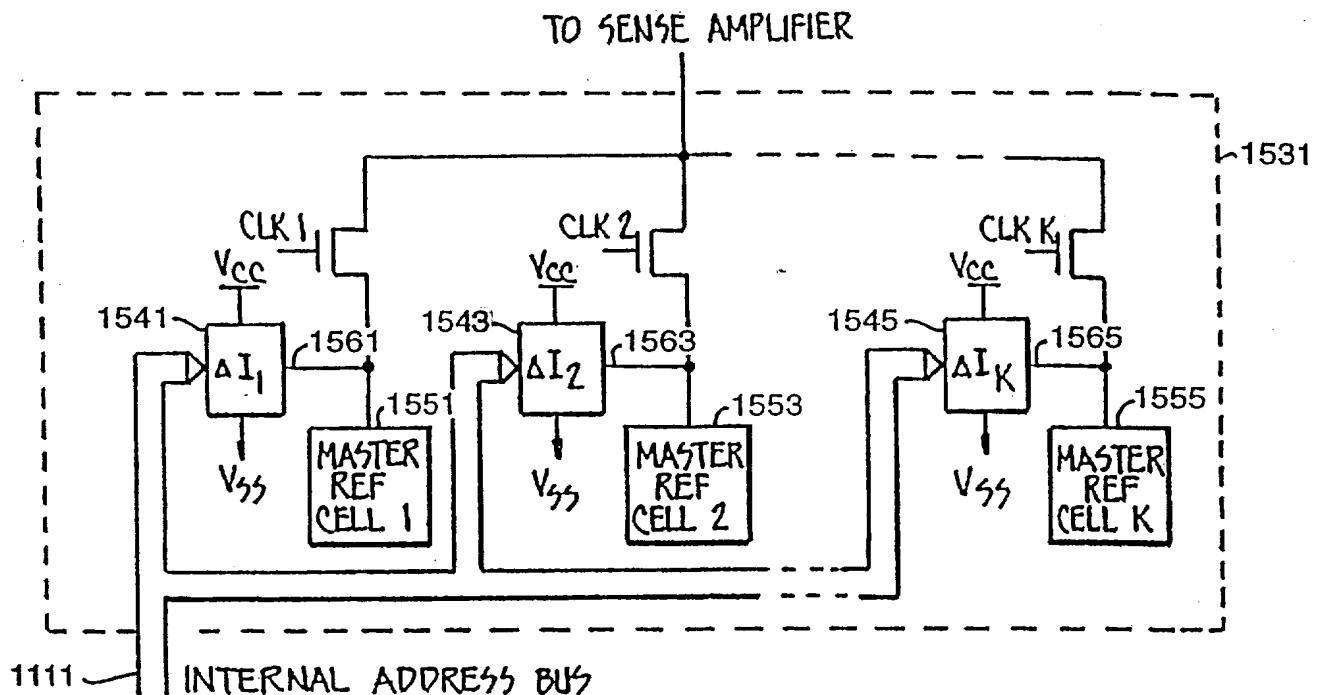


FIG. 21B

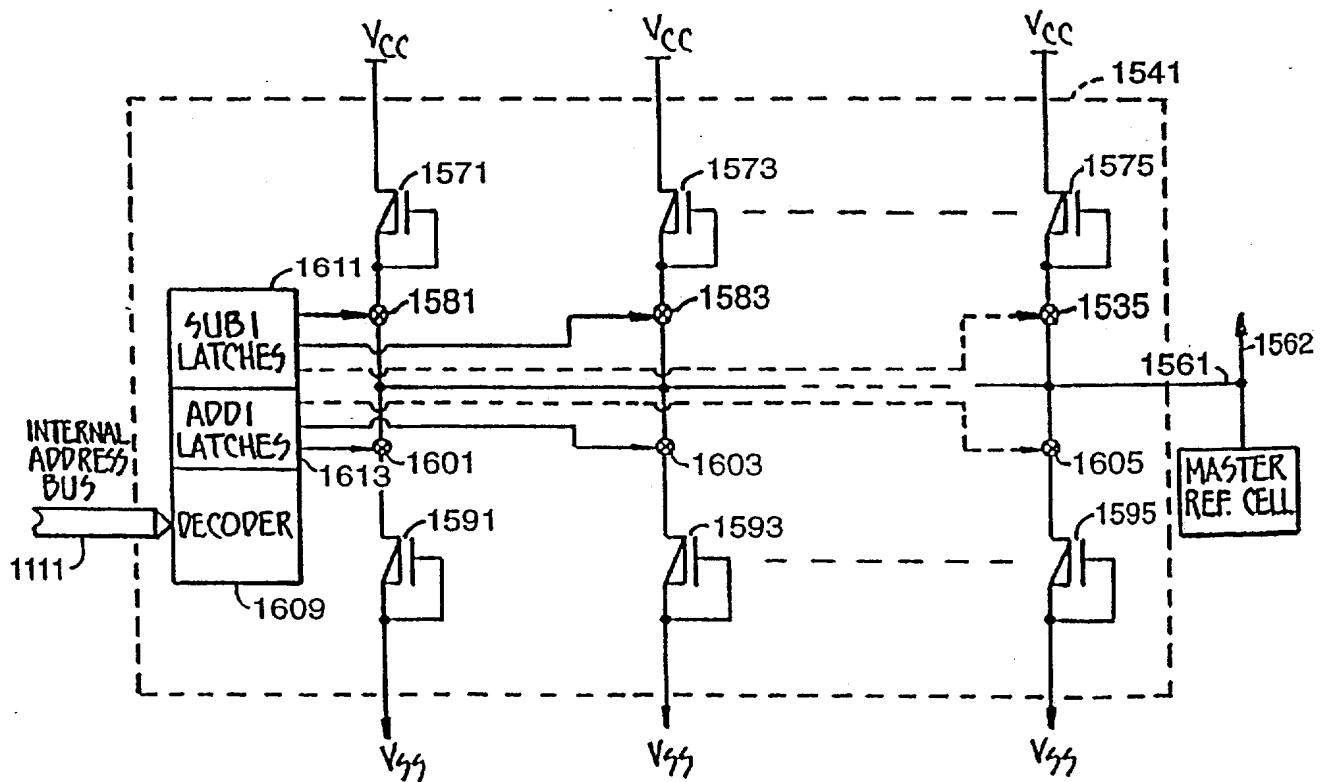
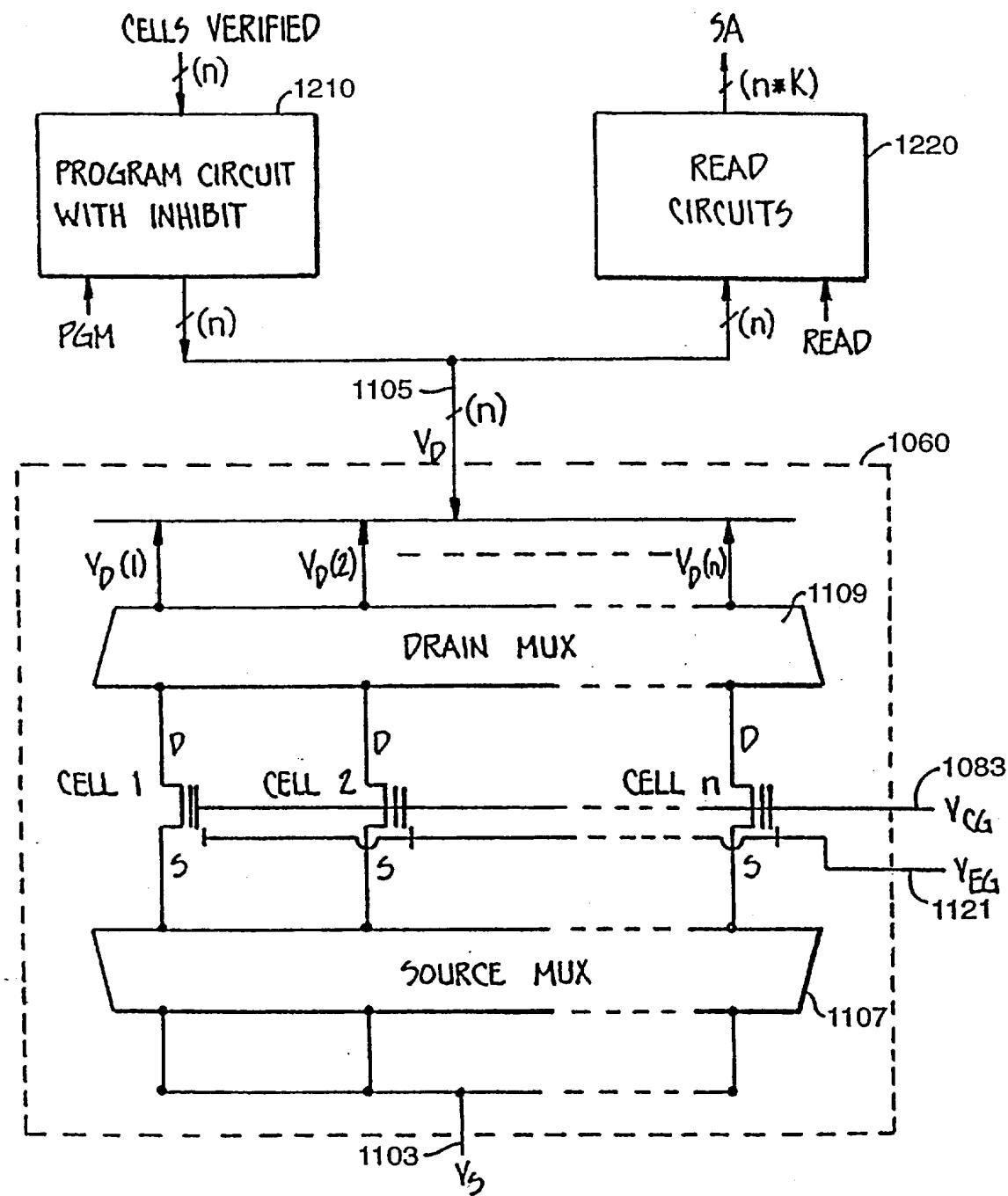


FIG. 21C

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READ/PROGRAM DATA PATHS FOR n CELLS IN PARALLEL

FIG._22